TOSHIBA Field Effect Transistor Silicon N Channel MOS Type T -MOSIV)

# 2SK3763

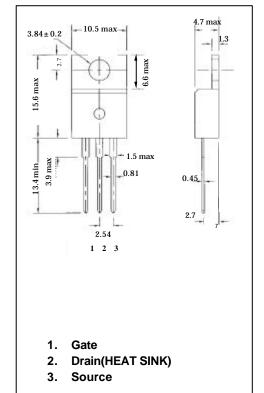
unit: mm

#### **Switching Regulator Applications**

- Low drain-source ON resistance: RDS (ON) = 3.7 (typ.)
- High forward transfer admittance:  $|Y_{fs}| = 2.6 \text{ S (typ.)}$
- Low leakage current:  $IDSS = 100 \mu A (VDS = 720 V)$
- Enhancement-mode:  $V_{th} = 2.0 \sim 4.0 \text{ V}$  ( $V_{DS} = 10 \text{ V}$ ,  $I_{D} = 1 \text{ mA}$ )

### Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Drain-source voltage		$V_{DSS}$	900	V
Drain-gate voltage ( $R_{GS} = 20 \text{ k}\Omega$ )		$V_{DGR}$	900	V
Gate-source voltage		$V_{GSS}$	±30	V
	DC (Note 1)	l <sub>D</sub>	3	Α
Drain current	Pulse (t = 1 ms) (Note 1)	l <sub>DP</sub>	9	
Drain power dissipation (Tc = 25°C)		$P_{D}$	69	W
Single pulse avalanche energy (Note 2)		E <sub>AS</sub>	56.7	mJ
Avalanche current		l <sub>AR</sub>	3	Α
Repetitive avalanche energy (Note 3)		E <sub>AR</sub>	6.9	mJ
Channel temperature		T <sub>ch</sub>	150	°C
Storage temperature range		T <sub>stg</sub>	-55~150	°C



JEDEC	TO-220AB
JEITA	SC-46
TOSHIBA	

Weight: 2.0g(typ.)

#### **Thermal Characteristics**

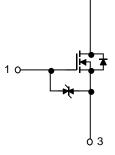
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	1.81	°C/W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	83.3	°C/W

Note 1: Please use devices on conditions that the channel temperature is below 150°C.

Note 2:  $V_{DD}$  = 90 V,  $T_{ch}$  = 25°C, L=11.6 mH,  $I_{AR}$  = 3.0 A,  $R_G$  = 25  $\Omega$ 

Note 3: Repetitive rating: Pulse width limited by maximum channel temperature

This transistor is an electrostatic sensitive device. Please handle with caution.



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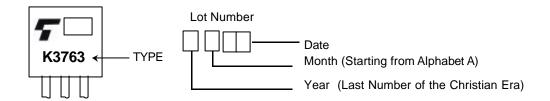
## Electrical Characteristics (Ta = 25°C)

Char	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	rrent	l <sub>GSS</sub>	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μΑ
Gate-source brea	akdown voltage	V (BR) GSS	$I_{G} = \pm 10 \mu A, V_{GS} = 0 V$	±30	_	_	V
Drain cut-off curr	ent	l <sub>DSS</sub>	$V_{DS} = 720 \text{ V}, V_{GS} = 0 \text{ V}$	_	_	100	μΑ
Drain-source bre	akdown voltage	V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	900	_	_	V
Gate threshold v	oltage	$V_{th}$	$V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA}$	2.0	_	4.0	V
Drain-source ON	I resistance	R <sub>DS (ON)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 1.5 A	_	3.7	4.3	Ω
Forward transfer	admittance	Y <sub>fs</sub>	$V_{DS} = 20 \text{ V}, I_D = 1.5 \text{ A}$	0.65	2.6	_	S
Input capacitanc	e	C <sub>iss</sub>		_	700	_	
Reverse transfer capacitance		C <sub>rss</sub>	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	15	_	pF
Output capacitance		C <sub>oss</sub>		_	75	_	
	Rise time	t <sub>r</sub>	10 V I <sub>D</sub> = 1.5 A V <sub>OUT</sub>	_	20	_	
Cusit alaine es time e	Second	_					
Switching time	Fall time	t <sub>f</sub>	\frac{1}{200 \text{ V}} \frac{1}{200 \text{ V}}	_	35	_	ns
	Turn-off time	t <sub>off</sub>		_	125	_	
Total gate charge	е	$Q_g$		_	17		
Gate-source charge		$Q_{gs}$	$V_{DD} \simeq 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 3 \text{ A}$		10		nC
Gate-drain charge		Q <sub>gd</sub>			7		

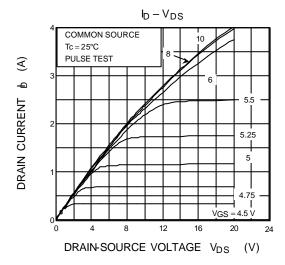
## Source-Drain Ratings and Characteristics (Ta = 25°C)

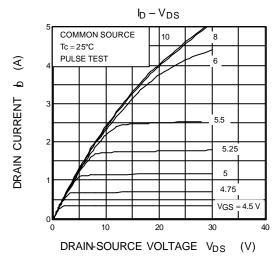
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	l <sub>DR</sub>	_	_	_	3	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_	_	9	Α
Forward voltage (diode)	$V_{DSF}$	$I_{DR} = 3 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	-1.9	V
Reverse recovery time	t <sub>rr</sub>	$I_{DR} = 3 A, V_{GS} = 0 V,$	_	850	_	ns
Reverse recovery charge	$Q_{rr}$	$dI_{DR}/dt = 100 A/\mu s$		4.7	_	μС

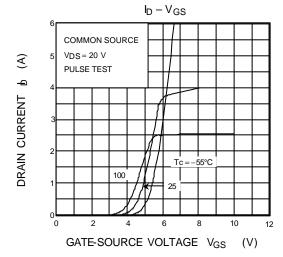
## Marking

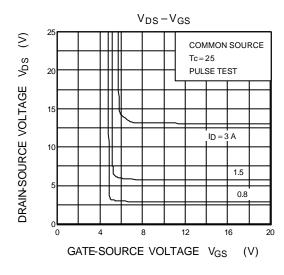


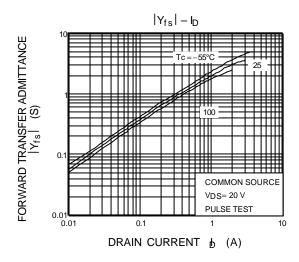
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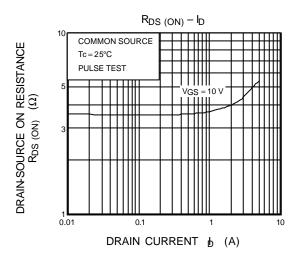


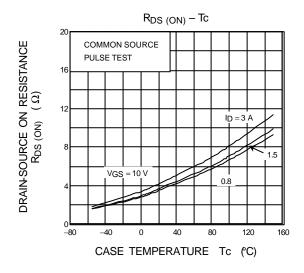


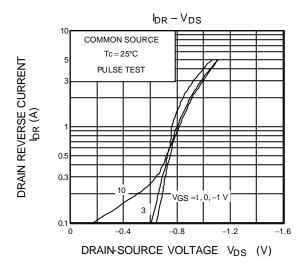


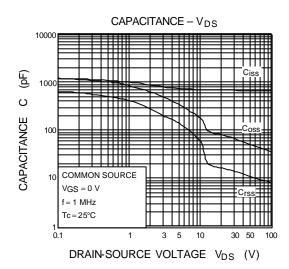


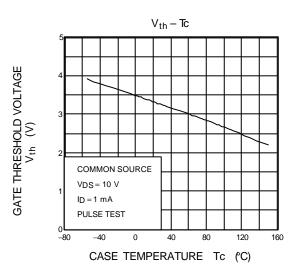


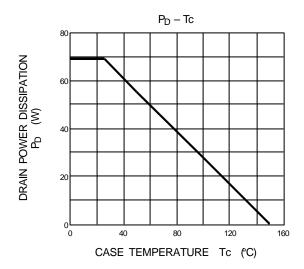


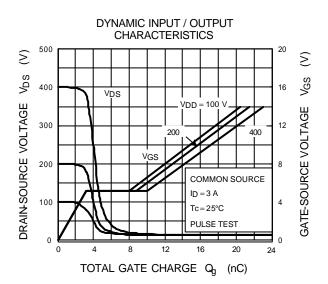


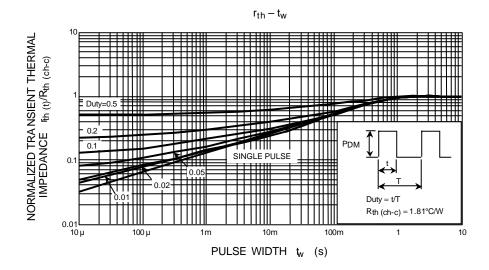


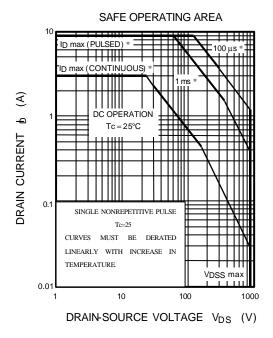


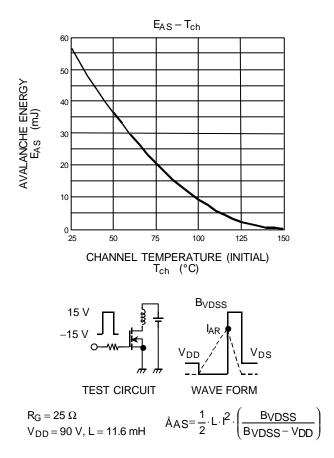












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